

Default in Debug Mode: All Pins GPIOs
 Default in Release Mode: GPIO0/GPIO1/USART_TX/USART_RX
 Pin Config can be reconfigured in Temperature Profile

USART serves a Shellmatta shell

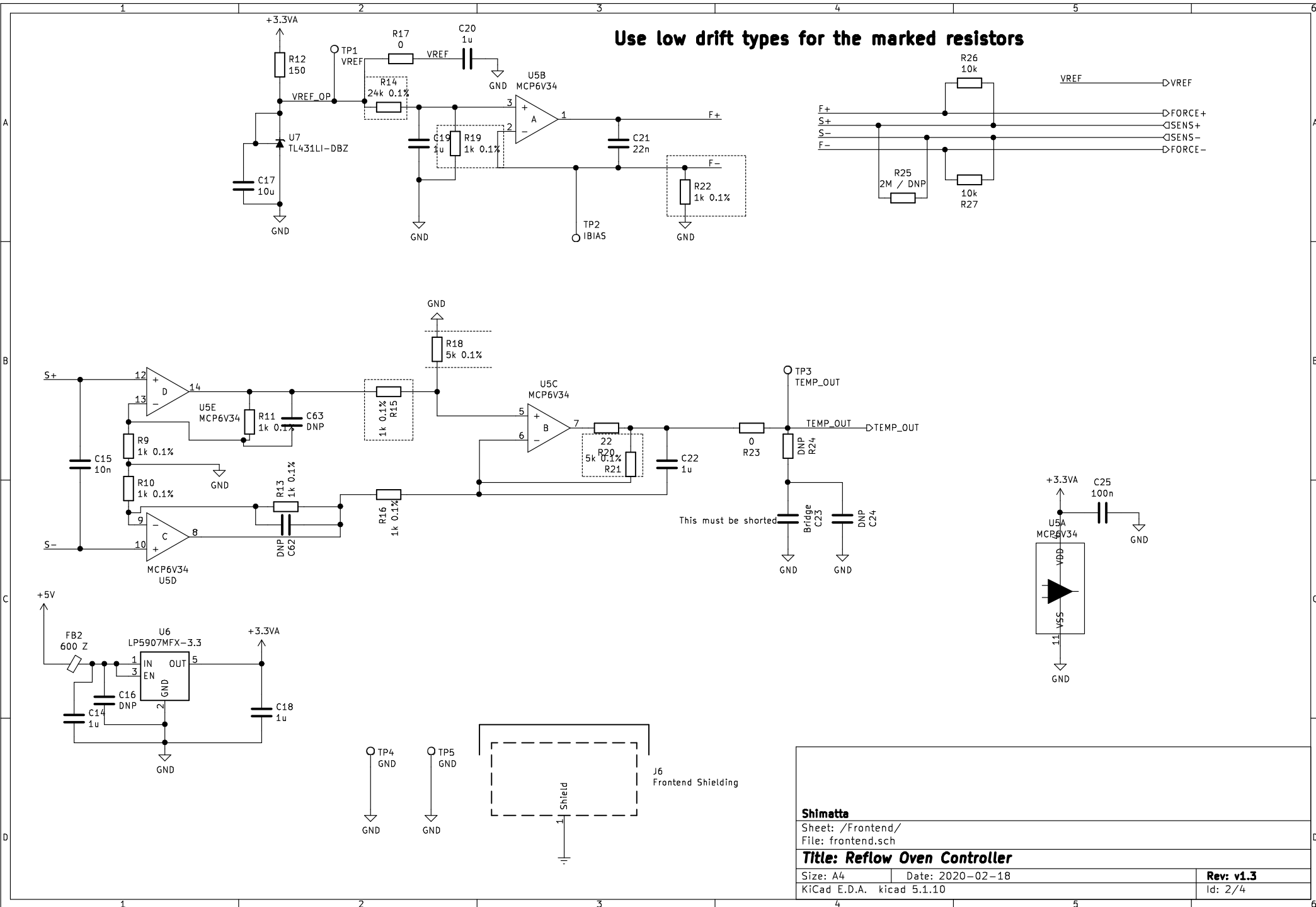
GPIO0	TIM3_CH1	
GPIO1	TIM3_CH2	CAN2_RX
GPIO2	USART1_TX	TIM4_CH1 I2C1_SCL CAN2_TX
GPIO3	USART1_RX	TIM4_CH2 I2C1_SDA

Possible, but not implemented in SW

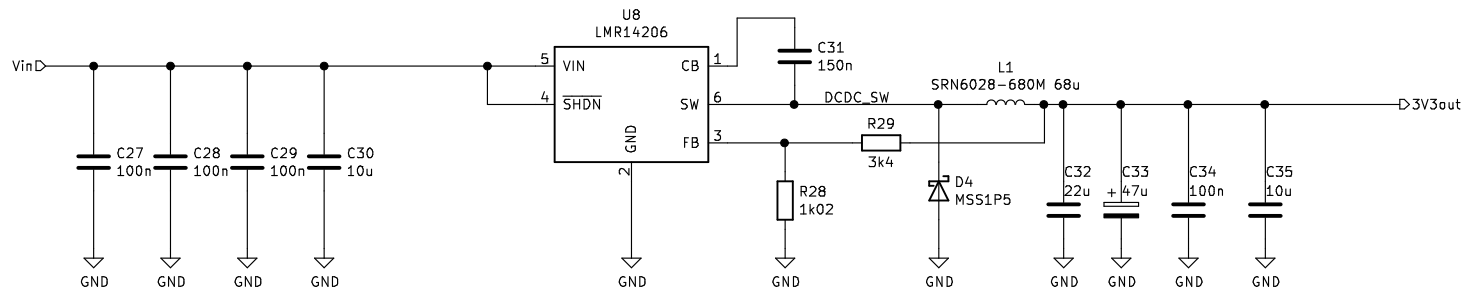
Shimatta
 Sheet: /
 File: reflow-oven-control-pcb.sch
Title: Reflow Oven Controller

Size: A4	Date: 2020-02-18
KiCad E.D.A. kicad 5.1.10	Rev: v1.3
	Id: 1/4

Use low drift types for the marked resistors



Shimatta	
Sheet: /Frontend/	
File: frontend.sch	
Title: Reflow Oven Controller	
Size: A4	Date: 2020-02-18
KiCad E.D.A. kicad 5.1.10	Rev: v1.3
	Id: 2/4



Sheet: /Digital Power Supply/
 File: digital-supply.sch

Title:

Size: A4
 KiCad E.D.A. kicad 5.1.10

Date:

Rev: v1.3

Id: 3/4

